

Electronic Acknowledgement Receipt

EFS ID:	8185338
Application Number:	10719020
International Application Number:	
Confirmation Number:	9168
Title of Invention:	METHOD FOR PREPARING A CIRCUIT BOARD MATERIAL HAVING A CONDUCTIVE BASE AND A RESISTANCE LAYER
First Named Inventor/Applicant Name:	Akira Matsuda
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Filer Authorized By:	Stephen G. Adrian
Attorney Docket Number:	032130
Receipt Date:	10-AUG-2010
Filing Date:	24-NOV-2003
Time Stamp:	13:02:16
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	yes
Payment Type	Credit Card
Payment was successfully received in RAM	\$ 1810
RAM confirmation Number	8702
Deposit Account	
Authorized User	

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
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1	Issue Fee Payment (PTO-85B)	032130_Issue_Fee_Transmittal_08_10_2010.pdf	91764 b789d6cdf436d0625111bcb1e42b91650114e49	no	1
Warnings:					
Information:					
2	Fee Worksheet (PTO-875)	fee-info.pdf	31932 ae175f82e49e9a1640d11aa4edf0c80e0f521e45	no	2
Warnings:					
Information:					
Total Files Size (in bytes):			123696		
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